

<b>Notice of References Cited</b>	Application/Control No. 10/695,624	Applicant(s)/Patent Under Reexamination TOHSCHÉ, ULF	
	Examiner Long Nguyen	Art Unit 2816	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	JP62-40816	02-1987	Japan	Sakagami, Kenji	
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Weste et al. (Principles of CMOS VLSI Design: A Systems Perspective, 1993, Addison Wesley, 2nd Edition, page 91
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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